

Atty. Dkt. No. 039153-0310 (F0797)

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Previously Presented) A method of increasing an etch stability of a photoresist layer, the method comprising the steps of:
providing the photoresist layer at a thickness less than 0.25 μm , for use in vacuum ultraviolet lithography, deep ultraviolet lithography, or extreme ultraviolet lithography;
exposing the photoresist layer to a plasma including fluorine, the photoresist layer including exposed surfaces; and
transforming the exposed surfaces to form a shell before using the photoresist layer to etch an underlying layer, wherein the shell increases the etch stability of the photoresist layer.
2. (Currently Amended) The method of claim 1, wherein the plasma ~~is a fluorine-based plasma~~ has a plasma characteristic of approximately 50 mTorr chamber pressure.
3. (Currently Amended) The method of claim 2, wherein the plasma ~~includes a CF_4 plasma having~~ has plasma characteristics of approximately 100 sccm flow rate, 50 mTorr ~~chamber pressure~~, 400 W radio frequency (RF) power, and 50 °C lower electrode temperature.
4. (Previously Presented) The method of claim 1, wherein the etch stability is increased by 20-50% as compared to the etch stability of the photoresist layer before exposure to the plasma.
5. (Original) The method of claim 1, wherein the transforming step includes transforming the exposed surfaces by a depth of up to approximately 50 nm.

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6. (Currently Amended) The method of claim 1, wherein the transforming step includes at least one of decarboxylation, cross-linking, and densification of the exposed surfaces to form the ~~hardened shell~~, the shell being hardened.

7. (Currently Amended) An integrated circuit fabrication process, the process comprising:

patterning a feature on a photoresist layer disposed over a substrate, the feature patterned in accordance with a pattern provided on a mask or reticle and a radiation at a deep ultraviolet or extreme ultraviolet lithographic wavelength;

developing the photoresist layer, the patterned photoresist layer including at least one feature having a top surface and side surfaces;

exposing the photoresist layer to a fluorine-based ~~or argon~~ plasma densifier;

transforming the top surface and the side surfaces with the densifier to form a hardened surface; and

etching the substrate in accordance with the transformed feature, wherein the exposing step occurs after the developing step and before the etching step, and an etch stability of the feature is a function of the hardened surface.

8. (Original) The process of claim 7, further comprising providing the photoresist layer at a thickness of less than approximately 0.25 μm .

9. (Currently Amended) The process of claim 7, wherein the ~~densifier is a fluorine-based plasma~~, transforming step includes decarboxylation.

10. (Original) The process of claim 9, wherein the exposing step includes providing the fluorine-based plasma at operating parameters of 10-2000 sccm flow rate, 5-1000 mTorr chamber pressure, 50-2000 W RF power, and 20-80 °C lower electrode temperature.

11. (Original) The process of claim 7, wherein the densifier is a flood electron beam.

12. (Original) The process of claim 7, wherein the densifier is an ion implantation.

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13. (Original) The process of claim 7, wherein the hardened surface has a depth of 2-50 nm.

14. (Original) The process of claim 7, wherein the transforming step includes at least one of decarboxylation, cross-linking, and densification of the top surface and the side surfaces to form the hardened surface.

15. (Currently Amended) ~~The process of claim 7~~ An integrated circuit fabrication process, the process comprising:

patterning a feature on a photoresist layer disposed over a substrate, the feature patterned in accordance with a pattern provided on a mask or reticle and a radiation at a deep ultraviolet or extreme ultraviolet lithographic wavelength;

developing the photoresist layer, the patterned photoresist layer including at least one feature having a top surface and side surfaces;

exposing the photoresist layer to a fluorine-based or argon densifier;

transforming the top surface and the side surfaces with the densifier to form a hardened surface; and

etching the substrate in accordance with the transformed feature, wherein the exposing step occurs after the developing step and before the etching step, and an etch stability of the feature is a function of the hardened surface, wherein the transforming step includes fluorinating the top surface and the side surfaces.

16.-23. (Cancelled).

24. (Currently Amended) A method of using a feature on a photoresist layer disposed above a layer or substrate, the feature including exposed surfaces, the feature being lithographically patterned using at least one of a deep ultraviolet lithographic wavelength, a vacuum ultraviolet lithographic wavelength, and an extreme ultraviolet lithographic wavelength or has a vertical thickness less than approximately 0.25 μm , and the method comprising:

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transforming the exposed surfaces to be structurally denser than the untreated region due to at least one of a fluorination, a fluorine ion implantation and argon ion implantation; and

etching or doping the layer or substrate according to the feature, wherein the exposed surfaces are structurally denser due to the fluorination, the fluorination including the feature being exposed to a fluorine-based plasma.

25. (Previously Presented) The method of claim 24, wherein a depth of the exposed surfaces is in the range of 2-50 nm.

26. (Previously Presented) The method of claim 24, wherein the exposed surfaces comprise a top surface and side surfaces of the feature.

27. (Currently Amended) The method of claim 24, wherein ~~the exposed surfaces are structurally denser due to the fluorination, the fluorination including the feature being exposed to a fluorine-based plasma.~~ the transforming step includes cross-linking.

28. (Currently Amended) The method of claim ~~27~~ 24, wherein the fluorine-based plasma has operating parameters of 10-2000 sccm flow rate, 5-1000 mTorr chamber pressure, 50-2000 W RF power, and 20-80 °C lower electrode temperature.

29. (Previously Presented) The feature of claim 24, wherein the material comprising the photoresist layer is an organic-based photoresist material based on at least one of phenolic polymers, acrylate polymers, and alicyclic polymers.

30. (Previously Presented) The method of claim 24, wherein the photoresist layer comprises an acrylate or alicyclic polymer and the feature is lithographically patterned using a 193 nm wavelength of radiation.

31. (Previously Presented) The method of claim 24, wherein the feature is at least one of a conducting line, a gate for a transistor device, a contact hole, a via structure, or a trench.